

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Yukio TANAKA

Serial No.: 10/034,797

Filed: December 28, 2001

Art Unit: 2819

Examiner: John B. Nguyen

For: D/A CONVERSION CIRCUIT AND
SEMICONDUCTOR DEVICE

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on 02/13/2004

Christine A. Barglik

Date: February 13, 2004

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. Copies of the references are enclosed herewith for the Examiner's consideration.

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

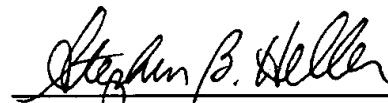
The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not

possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

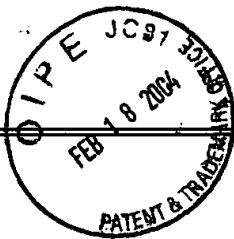
As a RCE is being filed herewith no fee is believed necessary. If a fee is required, please charge Deposit Account No. 50/1039.

Respectfully submitted,



Stephen B. Heller
Registration No: 30,181

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LIST OF PUBLICATIONS
CITED BY APPLICANT

Atty. Docket No.
SEL 205 DIV

Serial No.
10/034,797

Applicant
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Filing Date
December 28, 2001

Group
2819

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	4,398,207	08/09/83	Hoff, Jr. et al	357	51	05/12/81
	4,801,819	01/31/89	Theus et al	307	269	06/29/87
	5,170,158	12/08/92	Shinya	340	800	06/28/90
	5,570,105	10/29/96	Koyama	345	98	12/21/94
	5,572,211	11/05/96	Erhart et al	341	144	01/18/94
	5,589,847	12/31/96	Lewis	345	98	09/23/91
	5,648,277	07/15/97	Zhang et al	437	21	11/02/94
	5,731,774	03/24/98	Fujii et al	341	144	08/28/96
	5,793,348	08/11/98	Lee et al	345	98	11/09/95
	5,801,673	09/01/98	Shimada et al	345	100	08/29/94
	5,977,940	11/02/99	Akiyama et al	345	94	03/06/97
	6,040,812	03/21/00	Lewis	345	89	06/19/96
	6,091,390	07/18/00	Sim	345	98	07/09/97
	6,111,557	08/29/00	Koyama et al	345	89	12/29/97
	6,281,891 B1	08/28/01	DaCosta et al	345	206	06/02/95
	6,459,395 B1	10/01/02	Kida et al	341	126	10/27/00
	6,600,436 B2	07/29/03	Tanaka	341	150	03/25/02
	6,614,376 B2	09/02/03	Tanaka et al	341	144	11/21/01

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE
	JP 58-030231	02/22/83	Seiko Epson Corp.	X		08/18/81
	JP 06-208343	07/26/94	Toshiba KK	X		01/12/93
	JP 07-135318	05/23/95	Semiconductor	X		11/05/93
	JP 08-078329	03/22/96	Energy Lab Co. Ltd			
			Semiconductor	X		09/05/94
	JP 08-237097	09/13/96	Energy Lab Co. Ltd			
			Matsushita Denki	X		02/24/95
			Sangyo KK			
	JP 09-009275	01/10/97	Sony Corp.	X		06/16/95
	JP 11-142807	05/28/99	NEC IC Micro- computer Systems Ltd.	X		11/13/97



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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

- 1) LEWIS, A.G. et al, "Switched-Capacitor Circuits Using Polysilicon on Quartz Thin-Film Technology," ISSCC 92/Session 7/Circuits for Transducers and Active-Matrix LCD Drivers/Paper 7.3, IEEE International Solid State Circuits Conference, pp. 122-123 (1992).
- 2) FURUHASHI, T. et al, "A 64-Gray-Scale Digital Signal Driver for Color TFT-LCDs," SID 94 Digest, pp. 359-362 (1994).
- 3) MORITA, T. et al, "VGA Driving with Low Temperature Processed Poly Si TFTs," IDEM 95, pp. 841-843 (1995).
- 4) YAMAMOTO, Y. et al, "High Performance Low Temperature Poly-Si TFT with Self-Aligned Offset Gate Structure by Anodic Oxidation of Al for a Driver Monolithic LCDs," Asia Display '95, pp. 941-942 (1995).
- 5) MATSUEDA, S. et al, "Low-Temperature Poly-Si TFT-LCD with Integrated 6-bit Digital Data Drivers," SID 96 Digest, pp. 21-24 (1996).
- 6) KOYAMA, J. et al, "Low-Temperature Poly-Si TFT-LCDs with Digital Interface," Extended Abstracts of the 1997 International Conference on Solid State Devices and Materials, Hamamatsu, pp. 348-349 (1997).
- 7) OHTANI, H. et al, "Late-News Poster: A 60-in. HDTV Rear-Projector with Continuous-Grain-Silicon Technology," SID 98 Digest, pp. 467-470 (1998).
- 8) OSAME, M. et al, "Poly-Si TFT-LCD HDTV Display with Monolithic Integrated 8-bit Digital Data Drivers," SID 98 Digest, pp. 1059-1062 (1998).

EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.